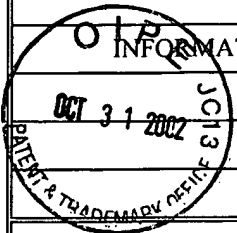


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						M-5648-1D US		09/458,551	
						INFORMATION DISCLOSURE STATEMENT BY APPLICANT			
(Use several sheets if necessary)						Applicant(s)			
						Alexander Joffe et al.			
						Filing Date		Group	
						December 9, 1999		2739	
U.S. Patent Documents									
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
	AA								
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Foreign Patent Documents									
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	AL								
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<i>MB</i>	AP	Farrens M. K. et al.: "Strategies For Achieving Improved Processor Throughput", Computer Architecture News, Association For Computing Machinery, New York, US, vol. 19, no. 3, 1 May 1991 (1991-05-01), pages 362-369.							
<i>MB</i>	AQ	Lee E. A. et al.: "Pipeline Interleaved Programmable DSP's: Architecture", IEEE Transactions On Acoustics, Speech And Signal Processing, IEEE Inc. New York, US, vol. 35, no. 9, 1 September 1987 (1987-09-01), pages 1320-1333.							
<i>MB</i>	AR	Nemirovsky M. D. et al.: "Disc: Dynamic Instruction Stream Computer", Proceedings Of The Annual International Symposium On Microarchitecture, XX, XX, 1991, pages 163-171.							
Examiner <i>Mojib Benamir</i>		Date Considered <i>2/27/04</i>							
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				Group 2700		M-5648-1D US		09/458,551	
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(Use several sheets if necessary)						Joffe, Alexander; Vhshetsky, Dmitry			
Group 2700						Filing Date		Group	
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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MB	AB	5,357,617	10/18/1994	Davis et al.	395	375	
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Foreign Patent Documents							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
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Examiner <i>Majid Barandeh</i>	Date Considered <i>2/27/04</i>
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(Use several sheets if necessary)				Joffe et al.				
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				Herewith		Unassigned		
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<i>MB</i>	AA	2,686,844	08/17/54	Brewer				
<i>MB</i>	AB	4,885,744	12/05/89	Lespagnol et al.	370	466		
<i>MB</i>	AC	5,062,106	10/29/91	Yamazaki	270	417		
<i>MB</i>	AD	5,233,606	08/03/93	Pashan et al.	270	418		
<i>MB</i>	AE	5,311,509	05/10/94	Heddes et al.	370	397		
<i>MB</i>	AF	5,337,308	08/09/94	Fan	270	414		
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<i>MB</i>	AH	5,517,495	05/14/96	Lund et al.	370	60		
<i>MB</i>	AI	5,528,588	06/18/96	Bennett et al.	370	60		
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<i>MB</i>	AL	WO	07/27/95	PCT				
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<i>MB</i>	AR	Alan Demers et al. "Analysis and Simulation Of A Fair Queueing Algorithm" ©1989 ACM, pp 1-12.						
<i>MB</i>	AS	"ATMS2003B Switch Controller 1 "White"", <u>MMC Networks</u> , December 1995, 32 pages.						
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(Use several sheets if necessary)				Joffe et al.				
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*Examiner		Document					Filing Date	
<i>MB</i>	AA	5,557,611	09/17/96	Cappellari et al.	270	414		
<i>MB</i>	AB	5,583,863	12/10/96	Darr, Jr. et al.	370	397		
<i>MB</i>	AC	5,592,476	01/07/97	Calamvokis et al.	270	413		
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<i>MB</i>	AE	5,633,859	05/27/97	Jain et al.	370	234		
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<i>MB</i>	AG	5,689,508	11/18/97	Lyles	370	391		
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<i>MB</i>	AI	5,715,250	02/03/98	Watanabe	370	395		
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<i>MB</i>	AQ	Beraldi, R., et al: "SELECTIVE BECN SCHEMES FOR CONGESTION CONTROL OF ABR TRAFFIC IN ATM LAN", 1996 IEEE Int'l Conf. on Communications (ICC), Converging Technologies for Tomorrow's Applications, Dallas, June 23-27, 1996. pgs. 503-507.						
<i>MB</i>	AR	Dutton, Harry, J.R. and Lenhard, Peter, "Asynchronous Transfer Mode (ATM) Technical Overview" (IBM, Prentice-Hall PTR, New Jersey, 1995), pp. 3-1 through 3-						
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<i>MB</i>	AA	5,751,951	05/12/98	Osborne et al.	395	200.8		
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<i>MB</i>	AS	Ozveren, C., et al: "Reliable and Efficient Hop-By-Hop Flow Control", <u>Computer Communications Review</u> , vol. 24, no. 4, 1 Oct. 1994, pgs. 89-100.						
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